IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Stephen L. Morein et al. Serial No. 10/820,580

Filing Date: April 8, 2004

Confirmation No.: 8567

Examiner: Hau H. Nguyen Art Group: 2628

Docket No.: 00100.02.0003

Title: TWO LEVEL CACHE MEMORY ARCHITECTURE

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE

Dear Sir:

In response to the Office Action mailed April 26, 2007, for the above-identified patent application, Applicants respond as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.